

SPECIFICATION

Please amend the Specification as follows:

Please replace the full paragraph beginning at Page 6, line 27 with the following:

Turning now to FIGURE 4, disclosed are the actions of the debugging program as related to the portion of the debugging program activated in the section of memory of the target SPU. After activation of the debugging call from debugger event loop 200 in FIGURE 1, the copy SPU's registers block 400, copies the selected register data from the SPU 102 into a block reserved memory allocation at MEM 103. Next, command block 402 issues a command to make a copy of the area of MEM 103 [[102]] holding the present register state and forward that copy to an allocated portion of CPU 100 memory. Concurrently, command block 402 copies indicia remaining in MEM 103, (which is unrelated to the register state) and forwards that copy to CPU 100. At a minimum, MEM 103 contains the salient data that causes the program halt. There can be additional code lines in MEM 103, which may be related or unrelated to the debugging operation. When the processing of command block 404 is complete, and output is sent to signal completion block 406, the debugger event loop 200 resets and waits for a subsequent program halt instruction.